

Abstract of the Disclosure

A current reference with reduced sensitivity to process variations includes a variable resistor and a control transistor. The control transistor has a current from source-to-drain that is provided by a current mirror. The current mirror also provides
5 a current to a variable resistor that is coupled gate-to-source to the control transistor.

A control loop circuit measures the reference current provided by the current mirror and modifies the resistance value of the variable resistor in response. An external precision resistor is used to measure the reference current, and current variations as a result of process variations are reduced.

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